| L Number | Hits | Search Text | DB | Time stamp | |
|----------|------|--|-----------|------------|--|
| - | 496 | 700/2 AND (bus network) AND (@ad<20010921 | USPAT; | 2004/02/24 | |
| | | @rlad<20010921) | US-PGPUB; | 07:45 | |
| | | | EPO; JPO; | | |
| | | | DERWENT: | | |
| | | | IBW_TDB | | |
| - | 68 | 700/2 and (carrier pcb circuit adj board) and bus and | USPAT; | 2004/02/24 | |
| | | (control\$4 regulat\$5) and (storage memory) and (input | US-PGPUB; | 07:49 | |
| | | output i/o) and (@ad<20010921 @rlad<20010921) | EPO; JPO; | | |
| | | | DERWENT; | | |
| İ | | | IBM_TDB | | |
| - | 10 | 700/2 and vehicle and (carrier pcb circuit adj board) and | USPAT; | 2004/02/24 | |
| | | bus and (control\$4 regulat\$5) and (storage memory) and | US-PGPUB; | 07:52 | |
| | | (input output i/o) and (@ad<20010921 @rlad<20010921) | EPO; JPO; | | |
| | | | DERWENT; | | |
| | | | IBM_TDB | | |
| - | 5 | 700/2 and vehicle and (carrier pcb circuit adj board) and | USPAT; | 2004/02/24 | |
| | | (communication data) near bus and (control\$4 regulat\$5) | US-PGPUB; | 07:52 | |
| | | and (storage memory) and (input output i/o) and | EPO; JPO; | | |
| | | (@ad<20010921 @rlad<20010921) | DERWENT; | | |
| | | | IBM_TDB | | |
| - | 5 | 700/2 and vehicle and (carrier pcb circuit adj board) and | USPAT; | 2004/02/24 | |
| | | (communication data) near bus and (storage memory) and | US-PGPUB; | 07:54 | |
| | | (input output i/o) and (@ad<20010921 @rlad<20010921) | EPO; JPO; | | |
| | | | DERWENT; | | |
| 1 | | | IBM_TDB | | |
| - | 0 | 700/2 and motor adj vehicle and (carrier pcb circuit adj | USPAT; | 2004/02/24 | |
| | | board) and (communication data) near bus and (storage | US-PGPUB; | 07:53 | |
| | | memory) and (input output i/o) and (@ad<20010921 | EPO; JPO; | | |
| | | @rlad<20010921) | DERWENT; | | |
| | | | IBM_TDB | | |
| - | 93 | 700/\$ and vehicle and (carrier pcb circuit adj board) and | USPAT; | 2004/02/24 | |
| | | (communication data) near bus and (storage memory) and | US-PGPUB; | 07:54 | |
| | | (input output i/o) and (@ad<20010921 @rlad<20010921) | EPO; JPO; | | |
| | | | DERWENT; | | |
| | | | IBW_TDB | | |
| - | 1 | 700/4 and vehicle and (carrier pcb circuit adj board) and | USPAT; | 2004/02/24 | |
| | | (communication data) near bus and (storage memory) and | US-PGPUB; | 07:54 | |
| | | (input output i/o) and (@ad<20010921 @rlad<20010921) | EPO; JPO; | | |
| | | | DERWENT; | | |
| | | | IBW_TDB | | |
| - | 0 | 700/4 and vehicle and (carrier pcb circuit adj board) | USPAT; | 2004/02/24 | |
| | | same (communication data) near bus and (storage | US-PGPUB; | 07:54 | |
| | | memory) and (input output i/o) and (@ad<20010921 | EPO; JPO; | | |
| | | @rlad<20010921) | DERWENT; | | |
| | | | IBM_TDB | | |
| - | 8 | 700/\$ and vehicle and (carrier pcb circuit adj board) | USPAT; | 2004/02/24 | |
| | | same (communication data) near bus and (storage | US-PGPUB; | 07:59 | |
| | , | memory) and (input output i/o) and (@ad<20010921 | EPO; JPO; | | |
| | | @rlad<20010921) | DERWENT; | | |
| | | | IBM_TDB | | |

| - | 1 | 700/\$ and motor adj vehicle and (carrier pcb circuit adj board) same (communication data) near bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 08:54 |
|---|-----|--|---|---------------------|
| - | 1 | 709/\$ and motor adj vehicle and (carrier pcb circuit adj board) same (communication data) near bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 08:01 |
| - | 9 | 307/\$ and motor adj vehicle and (carrier pcb circuit adj board) same (communication data) near bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 08:03 |
| - | 17 | 340/\$ and motor adj vehicle and (carrier pcb circuit adj board) same (communication data) near bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 08:04 |
| - | . 6 | 701/\$ and motor adj vehicle and (carrier pcb circuit adj board) same (communication data) near bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 09:01 |
| - | 3 | 700/\$ and motor adj vehicle and (carrier pcb circuit adj board) same bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 08:55 |
| - | 2 | 709/\$ and motor adj vehicle and (carrier pcb circuit adj board) same bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 08:56 |
| - | 20 | 307/\$ and motor adj vehicle and (carrier pcb circuit adj board) same bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 08:57 |
| - | 1 | 714/\$ and motor adj vehicle and (carrier pcb circuit adj board) same (communication data) near bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 09:01 |
| - | 45 | 340/\$ and motor adj vehicle and (carrier pcb circuit adj board) same bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 08:57 |
| - | 3 | 123/\$ and motor adj vehicle and (carrier pcb circuit adj board) same (communication data) near bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 09:02 |

| - 18 | 701/\$ and motor adj vehicle and (carrier pcb circuit adj | USPAT; | 2004/02/24 |
|------|--|---|---------------------|
| | board) same bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | US-PGPUB; EPO; JPO; DERWENT; | 09:02 |
| 6 | 714/\$ and motor adj vehicle and (carrier pcb circuit adj board) same bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 09:03 |
| 9 | 123/\$ and motor adj vehicle and (carrier pcb circuit adj board) same bus and (storage memory) and (input output i/o) and (@ad<20010921 @rlad<20010921) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 09:10 |
| | ("6430478" "5525959" "6684152" "4630220" "5367600" "4480298" "4884204" "4119353" "4461003" "5280232" "5291962" "5388091" "5831411" "6157175" "6377352" "5616965" "5892893" "6144312" "4267569" "4293947" "4410000" "4453113" "4784510" "4959962" "5216674" "5247705" "5281900" "5283485" "5339782" "5347109" "355573" "5357518" "5444626" "5617282" "5778528" "5791933" "5974351" "6152588" "6208924" "6429790" "4931964" "6085134" "6650976" "4459591" "4926352" "5025350" "5332279" "6038500" "5182460" "5601142").pn. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/24 09:12 |